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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 09/876,290 06/07/2001 Yoshiyuki Yanagisawa 09792909-5046 33448 7590 12/30/2003 **EXAMINER** ROBERT J. DEPKE LEWIS T. STEADMAN GRAYBILL, DAVID E HOLLAND & KNIGHT LLC ART UNIT PAPER NUMBER 131 SOUTH DEARBORN 30TH FLOOR 2827 CHICAGO, IL 60603

Please find below and/or attached an Office communication concerning this application or proceeding.

.S. Patent and T	rademark Office ev. 11-03)	Office Action Summary		D + f.D	N. 40000
2) Notice	e of Draftsperson's Patent Drawing Review (P nation Disclosure Statement(s) (PTO-1449) P	PTO-948)		Informal Patent Application (PTO-1	
Attachmen	t(s) e of References Cited (PTO-892)	,	I) Interview	r Summary (PTO-413) Paper No(s).	
12)	The oath or declaration is objected to under 35 U.S.C. §§ 119 and 120 Acknowledgment is made of a claim All b) Some * c) None of: 1. Certified copies of the priority 2. Certified copies of the priority 3. Copies of the certified copies application from the Internation of the attached detailed Office action of the capital of the certified copies application from the Internation of the attached detailed Office action of the attached detailed Office action of the specific reference was included of the foreign large of the complex consultation of the foreign large of the complex of the complex of the foreign large of the complex o	documents have been documents have been documents have been of the priority documer and Bureau (PCT Rule on for a list of the certificated in the first sentence on guage provisional apport of domestic priority under the priority under the first sentence of the certification of the first sentence of the priority under th	er 35 U.S.C received. received in its have bee 17.2(a)). ed copies no der 35 U.S.C of the specif lication has der 35 U.S.C	. § 119(a)-(d) or (f). Application No In received in this National Stort received. C. § 119(e) (to a provisional a lication or in an Application Date been received. C. §§ 120 and/or 121 since a second	age pplication) ata Sheet. specific
	Applicant may not request that any obje Replacement drawing sheet(s) including	g the correction is required	f if the drawir	ng(s) is objected to. See 37 CFR	, ,
	The specification is objected to by th The drawing(s) filed on is/are:] objected t	by the Examiner.	
Applicati	on Papers				
6)⊠ 7)□	Claim(s) is/are allowed. Claim(s) 1-7 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restrict	ction and/or election red	quirement.		
_	Claim(s) <u>1-10</u> is/are pending in the a 4a) Of the above claim(s) <u>8-10</u> is/are	- •	deration.		
·	on of Claims				
	Since this application is in condition closed in accordance with the practi				ients is
2a)□		2b)⊠ This action is nor			
1)⊠	Responsive to communication(s) file				
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	T. 45411 (NO DATE 541)	David E Gra		2827	
	Office Action Summary	Examiner		Art Unit	
		09/876,290	1	YANAGISAWA ET A	L.
		Application	I No.	Applicant(s)	

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Claims 8-10 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected invention, there being no allowable generic or linking claim. Election was made **without** traverse in the paper filed 8-21-3.

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 2 and 3 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claims 2 and 3 the scope of the term "box-shaped" cannot be determined because a box does not have a particular defined shape.

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-7 are rejected under 35 U.S.C. 102(b) as being anticipated by Levy (5869353).

At column 4, line 8 to column 8, line 21, Levy teaches the following:

1. A multilayer semiconductor device assembly jig, comprising: a lateral position restriction mechanism for positioning a plurality of stacked

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semiconductor modules 12 on a base member 68 with their respective lateral positions mutually restricted; a height restriction mechanism 80 for restricting an entire height of said semiconductor modules layered on said base member, and an alignment mechanism for providing alignment with reference to a mother substrate 74, and further wherein a plurality of the semiconductor modules are each comprised of a single semiconductor chip 14 secured to a printed wiring board 22 that has electrical connections 26, 60 on a top and bottom surface thereof and wherein a plurality of adjacent printed wiring board members are secured to one another by solder connections between top and bottom surfaces thereof.

- 2. The multilayer semiconductor device assembly jig according to 1 comprising a box-shaped member 74, 80 which is positioned on said base member and having a storage space for storing said semiconductor modules in a layered state, wherein an inner wall of said storage space constitutes said lateral position restriction mechanism.
- 3. The multilayer semiconductor device assembly jig according to 2, wherein said alignment mechanism comprises a plurality of positioning pins 70 and positioning holes 76 for receiving the positioning pins which are correspondingly formed in said box-shaped member and said mother substrate.

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4. The multilayer semiconductor device assembly jig according to 1, wherein said position restriction mechanism further comprises a plurality of positioning pins 70 secured in said base member and which are used for securing at least three different portions of an outer periphery of said
5. The multilayer semiconductor device assembly jig according to 1, wherein said position restriction mechanism further comprises a plurality of positioning pins secured in said base member and which pierce through positioning holes 36 formed in said semiconductor modules.

- 6. The multilayer semiconductor device assembly jig according to 5, wherein said positioning pins also pierce through a positioning hole formed on said mother substrate.
- 7. The multilayer semiconductor device assembly jig according to 1, wherein said height restriction mechanism further comprises: a cover member 74 secured over said semiconductor modules.

Any telephone inquiry of a general nature or relating to the status (MPEP 203.08) of this application or proceeding should be directed to Group 2800 Customer Service whose telephone number is 703-306-3329.

Any telephone inquiry concerning this communication or earlier communications from the examiner should be directed to David E. Graybill at (703) 308-2947, or after about 02/05/04, (571) 272-1930. Regular office hours: Monday through Friday, 8:30 a.m. to 6:00 p.m. The fax phone number for group 2800 is (703) 872-9306.

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David E. Graybill Primary Examiner Art Unit 2827

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